

44th International Semiconductor Conference CAS 2021

October 6 - 8, 2021, Romania



The International Semiconductor Conference - CAS stands as an international platform for sharing experience, novel results and breakthroughs in micro- and nanotechnologies, focusing on semiconductor research and development.

The conference program will include

- plenary sessions with high-level invited speakers
- oral presentations and special sessions for Student papers
- posters sessions
- awards for Best paper in each session and Best student papers

Don't miss the chance to be part of CAS 2021 program and submit your paper!

Important dates:

Regular papers submission: **June 4, 2021**

Notification of papers acceptance: **July 16, 2021**

Final submission deadline: **July 23, 2021**

The accepted papers (4 pages) will be published in the Conference Proceedings. Accepted papers will be submitted for inclusion into IEEE Xplore subject to meeting IEEE Xplore's scope and quality requirements.

Alternative option: submit papers of 2 pages, which could be selected for presentation at the conference only and will not be published in the Conference Proceedings.

Extended versions of selected papers presented at the conference may be included in special issues of indexed journals. The contributions will be refereed according to the standards of the Journals.

Organizing Committee:

General Chairman: Dr. Miron Adrian Dinescu - IMT Bucharest
Technical Program Chair: Dr. Mircea Dragoman - IMT Bucharest
Technical Program Co-Chairs: Prof. Gheorghe Brezeanu - "Politehnica" University of Bucharest
Dr. Raluca Müller - IMT Bucharest, Romania

Organiser:

National Institute for Research and
Development in Microtechnologies
- IMT Bucharest

FIRST CALL FOR PAPERS



Authors are invited to submit original papers on the following TOPICS:

- Nanoscience and Nanoengineering
- Micro- and Nanophotonics and Optoelectronics
- Microwave and Millimeter Wave Circuits and Systems
- Microsensors and Microsystems
- Modelling
- Semiconductor devices
- Integrated circuits

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